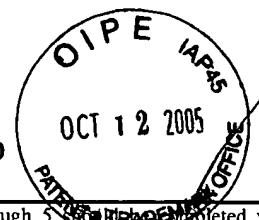


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22852 7590 07/22/2005

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER
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901 NEW YORK AVENUE, NW
WASHINGTON, DC 20001-4413

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03 FC:1504 65.00 OP

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/660,559	09/12/2003	Kouji Matsuo	04329.3138	6404

TITLE OF INVENTION: SEMICONDUCTOR DEVICE INCLUDING A GATE INSULATING FILM ON A RECESS AND SOURCE AND DRAIN EXTENSION REGIONS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	10/24/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
WEISS, HOWARD	2814	257-330000

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☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

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1 Finnegan, Henderson,
 2 Farabow, Garrett &
 3 Dunner, L.L.P.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kabushiki Kaisha Toshiba

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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Date

Typed or printed name

Richard V. Burgujian

Registration No.

31,744

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